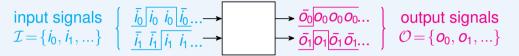


Reactive Synthesis in a Nutshell

A **reactive controller** produces output as a reaction to its input



Reactive Synthesis in a Nutshell

A reactive controller produces output as a reaction to its input

input signals
$$\mathcal{I} = \{i_0, i_1, ...\}$$

$$\begin{cases} \underline{i_0} \ i_0 \ i_0 \ \underline{i_0} ... \rightarrow \\ \underline{i_1} \ \underline{i_1} \ \underline{i_1} \ \underline{i_1} ... \rightarrow \end{cases}$$

$$\xrightarrow{\underline{o_0}} \boxed{o_0 o_0 o_0 o_0} ...$$
 output signals
$$\xrightarrow{\underline{o_1}} \boxed{o_1} \boxed{o_1} \boxed{o_1} \boxed{o_1} \boxed{o_1} ...$$

$$\mathcal{O} = \{o_0, o_1, ...\}$$

The reactive synthesis problems

Given a specification relating input signals and output signals over time:

Realizability: decide if a controller exists;

our focus

Synthesis: construct it (e.g., as an And-Inverter Graph).

Reactive Synthesis in a Nutshell

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$$\xrightarrow{\overline{O_0}} \underbrace{\overline{O_0}O_0O_0O_0...}_{O_1}$$
 output signals
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The reactive synthesis problems

Given a specification relating input signals and output signals over time:

Realizability: decide if a controller exists;

Synthesis: construct it (e.g., as an And-Inverter Graph).

Semantics for an LTL_f specification

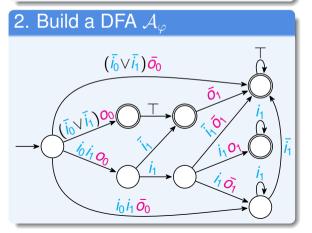
Any execution of the controller, seen as an infinite word such as $(\bar{l}_0 \bar{l}_1 \bar{o}_0 \bar{o}_1; i_0 \bar{l}_1 o_0 o_1; i_0 i_1 o_0 \bar{o}_1; ...]$, must have a finite prefix satisfying the specification.

1. LTL_f specification φ

 $(i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^! X^! o_1)$

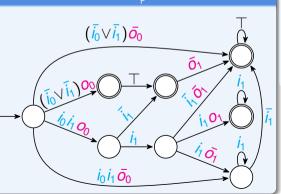
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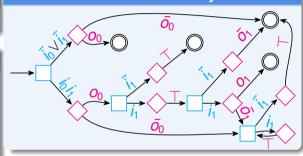


1. LTL_f specification φ $(i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$

2. Build a DFA \mathcal{A}_{ω}

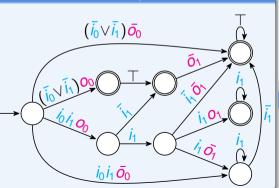


3. Make it a Reachability Game

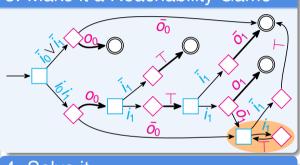


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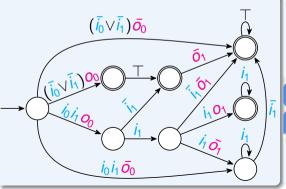
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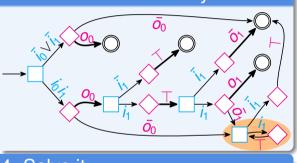
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1. LTL_f specification φ $(i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$

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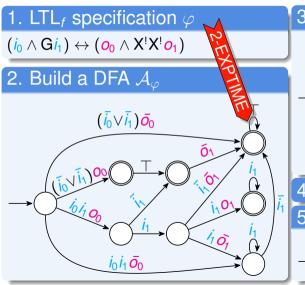


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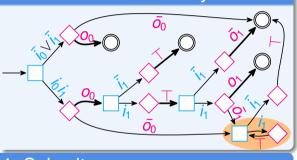


- 4. Solve it
- 5. Extract a Controller if Desired



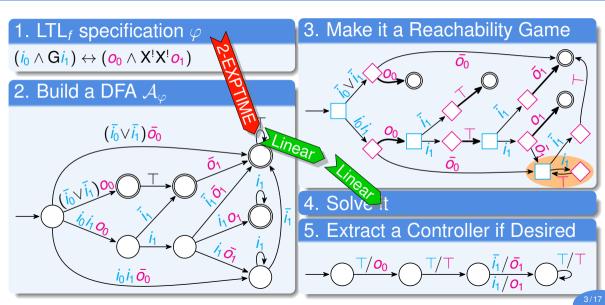


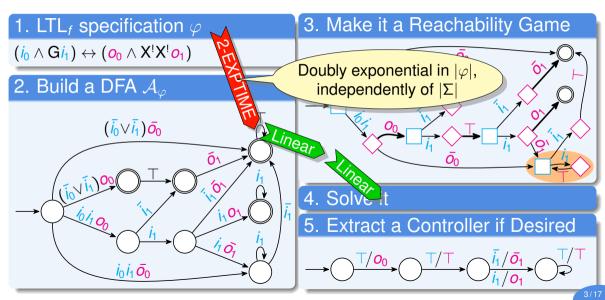
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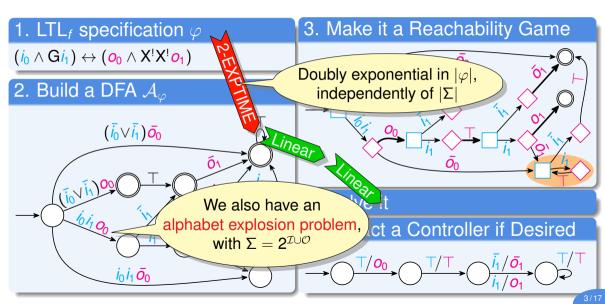


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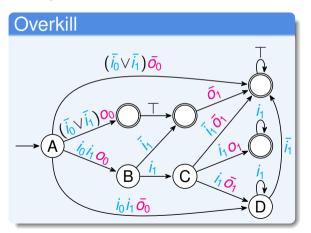






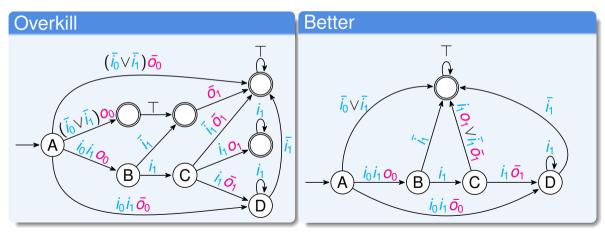
Stopping the DFA Construction on Final States

The goal is to reach final states: we do not care about what follows.



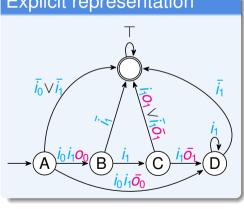
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Fighting Alphabet Explosion with MTBDDs

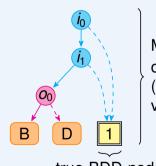
Semi-symbolic representation: MTBDD Explicit representation



Fighting Alphabet Explosion with MTBDDs

Explicit representation

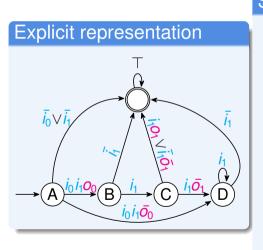
Semi-symbolic representation: MTBDD



MTBDD representing outgoing edges of (A) (unique for a given variable order)

true BDD node as accepting sink

Fighting Alphabet Explosion with MTBDDs



Semi-symbolic representation: MTDFA Array of roots **MTBDDs** representing outgoing edges (will share nodes) B true/false BDD nodes as accepting/rejecting sinks

LTL_f Synthesis with MTDFA

What Other Tools Have Tried

MTDFA Constructions:

- ► Transform LTL_f \rightarrow FOL, then use Mona for FOL \rightarrow MTDFA.
- Use Mona's MTDFA library to translate LTL_f to MTDFA by composition.

Game Solving: convert MTDFA to BDD, and solve symbolically. **Also exist on-the-fly approaches** that do not go through MTDFAs.

What we Suggest

- Direct translation from LTL_f to MTBDD, building the MTDFA one state at a time.
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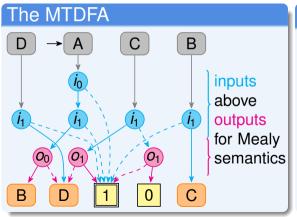
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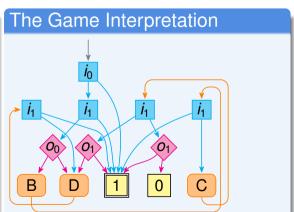
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Turn input/output nodes into universal/existential vertices.

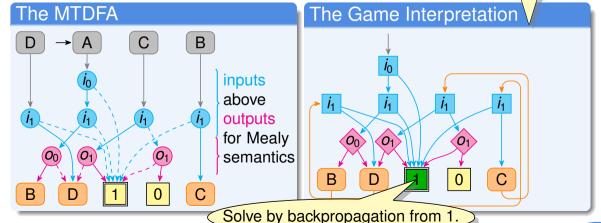
Order input/output variables according to the desired semantics (Moore/Mealy).





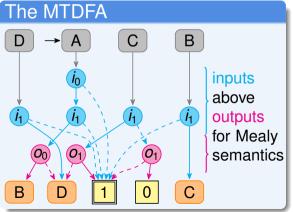
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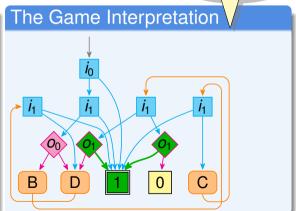
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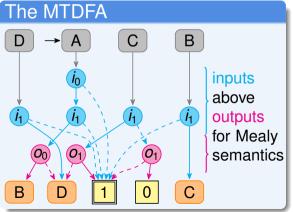
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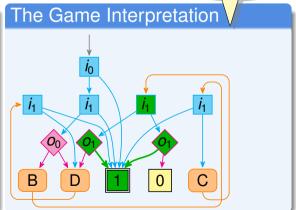




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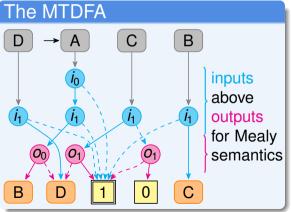
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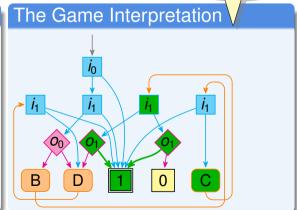




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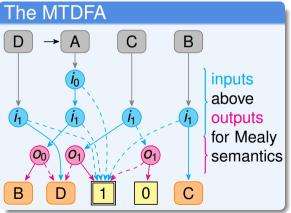
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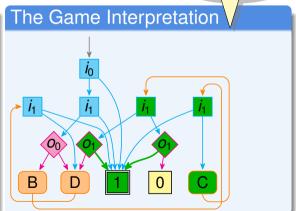




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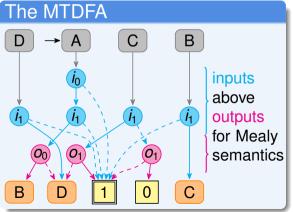
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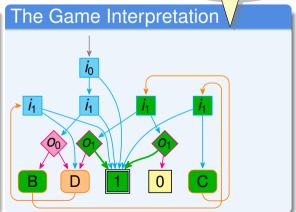




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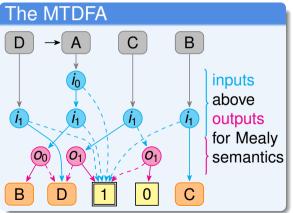
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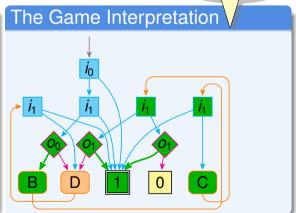




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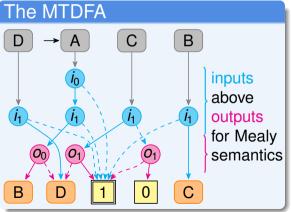
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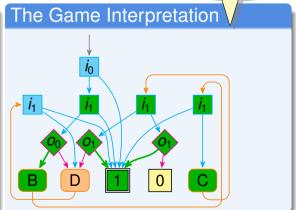




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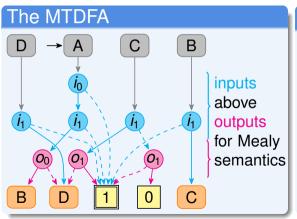
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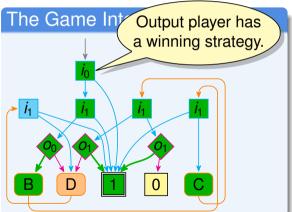




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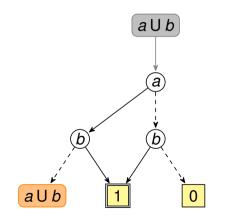
LTL_f Synthesis with MTDFA

What we Suggest

- Direct translation from LTL_f to MTBDD, building the MTDFA one state at a time.
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Use LTL_f formulas as terminals.

Assume we know an MTBDD for the successors of a U b

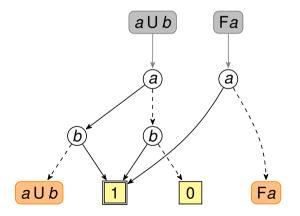


This is a deterministic representation of the *next normal form* (XNF):

$$a \cup b \equiv b \vee (a \wedge X^{!}(a \cup b))$$

Use LTL_f formulas as terminals.

Assume we know an MTBDD for the successors of a U b, and another for Fa.



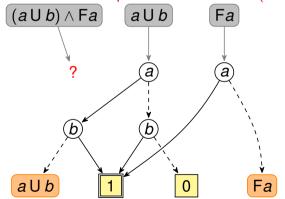
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 $Fa \equiv a \vee X^{!}Fa$

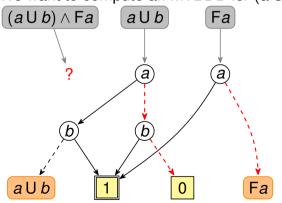
Use LTL_f formulas as terminals.

Assume we know an MTBDD for the successors of $a \cup b$, and another for Fa. We want to compute an MTBDD for $(a \cup b) \land Fa$:



Use LTL_f formulas as terminals.

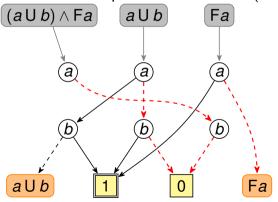
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Classical BDD apply procedure, but combine terminals with " \wedge ".

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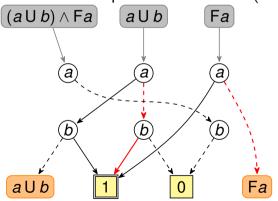
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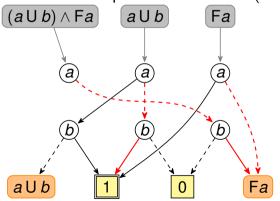
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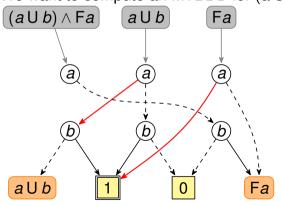
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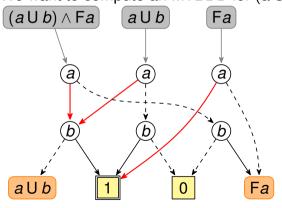
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Classical BDD apply procedure, but combine terminals with "\". Leaves 0 and 1 can help shortcut the recursion.

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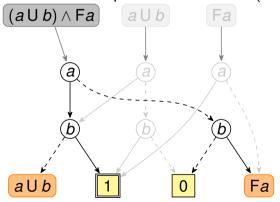


Classical BDD apply procedure, but combine terminals with "\lambda".

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Use LTL_f formulas as terminals.

Assume we know an MTBDD for the successors of $a \cup b$, and another for Fa. We want to compute an MTBDD for $(a \cup b) \land Fa$:



MTBDDs for subformulas are cached (i.e., not thrown away) in case they are needed later during the construction.

From LTL_f to MTBDD: Formal Definition

$$tr(ff) = 0 \qquad tr(X\alpha) = \alpha$$

$$tr(tt) = 1 \qquad tr(X^!\alpha) = \alpha$$

$$tr(p) = p \qquad \text{for } p \in \mathcal{I} \cup \mathcal{O} \qquad tr(\neg \alpha) = \neg tr(\alpha)$$

$$tr(\alpha \odot \beta) = tr(\alpha) \odot tr(\beta) \text{ for any } \odot \in \{\land, \lor, \rightarrow, \leftrightarrow, \oplus\} \qquad \text{previous slide}$$

$$tr(\alpha \cup \beta) = tr(\beta) \lor (tr(\alpha) \land \alpha \cup \beta) \qquad tr(F\alpha) = tr(\alpha) \lor F\alpha$$

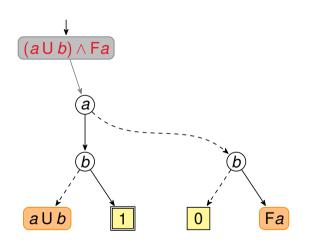
$$tr(\alpha \cap B \beta) = tr(\beta) \land (tr(\alpha) \lor \alpha \cap B \beta) \qquad tr(G\alpha) = tr(\alpha) \land G\alpha$$

With the convention that $\alpha \land \beta = \alpha \land \beta$, $\alpha \lor \beta = \alpha \lor \beta$, ...

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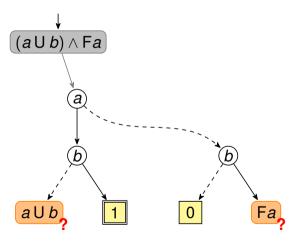
To translate $(a \cup b) \wedge Fa$:

• Compute successors of the initial state: $tr((a \cup b) \land Fa))$.

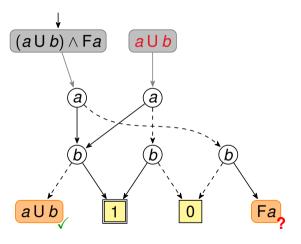


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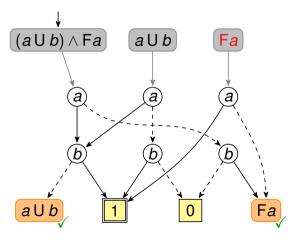
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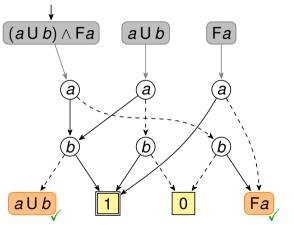
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- 2 Compute successors for each new terminal:



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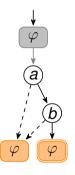


- Compute successors of the initial state: tr((a U b) ∧ Fa)).
- 2 Compute successors for each new terminal:
 - ightharpoonup tr($a \cup b$) (cached)
 - ► tr(Fa) (cached)

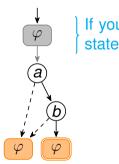


- Compute successors of the initial state: $tr((a \cup b) \land Fa)$).
- 2 Compute successors for each new terminal:
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 - ► tr(Fa) (cached)
- One.

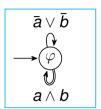
Our use of accepting terminals differs from Mona's implementation of MTDFAs.



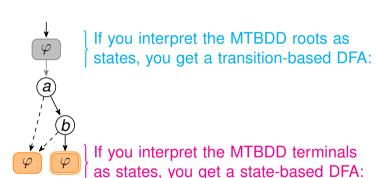
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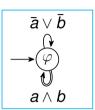


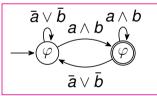
If you interpret the MTBDD roots as states, you get a transition-based DFA:



Our use of accepting terminals differs from Mona's implementation of MTDFAs.







Our use of accepting terminals differs from Mona's implementation of MTDFAs.



In any case, when using an MTDFA for synthesis, accepting terminals can all be replaced by the accepting sink 1

LTL_f Synthesis with MTDFA

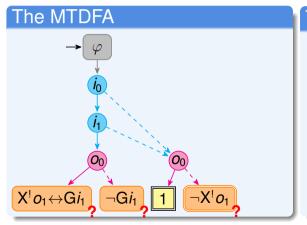
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$$\varphi = (i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$$

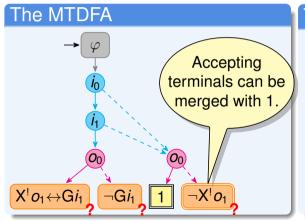


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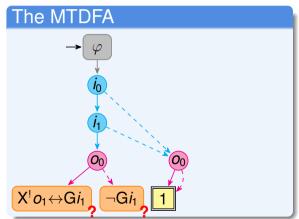


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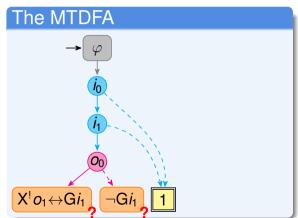
The Game Interpretation

$$\varphi = (i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$$



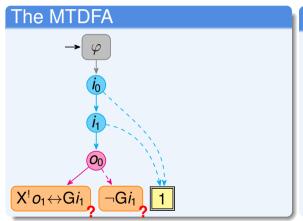
The Game Interpretation

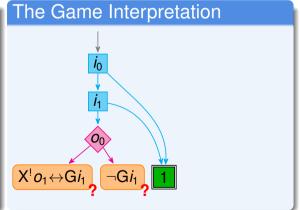
$$\varphi = (i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$$



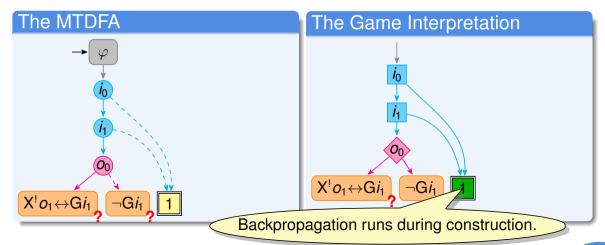
The Game Interpretation

$$\varphi = (i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$$

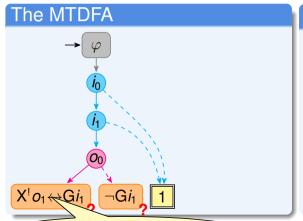


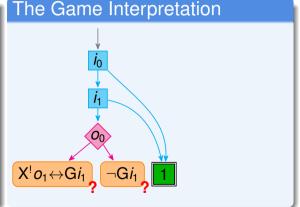


$$\varphi = (i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$$



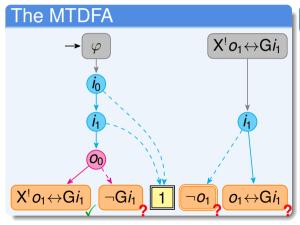
$$\varphi = (i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$$

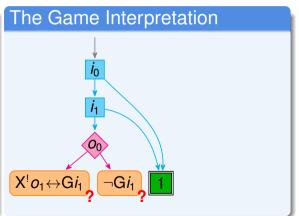




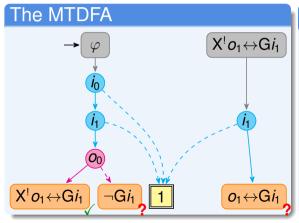
Pick another terminal to develop.

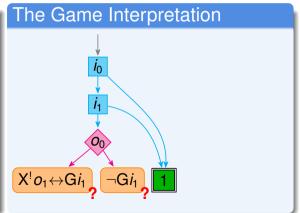
$$\varphi = (i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$$



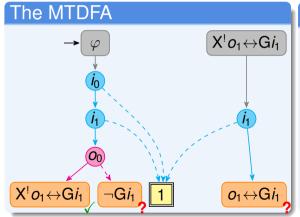


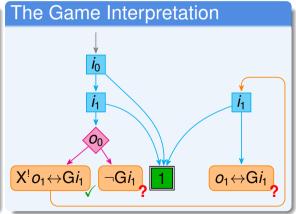
$$\varphi = (i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$$



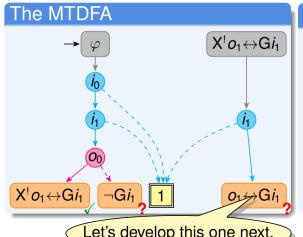


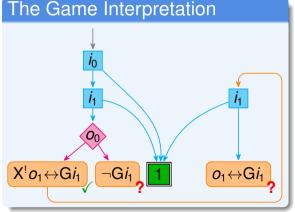
$$\varphi = (i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$$





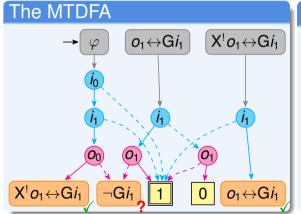
$$\varphi = (i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^! X^! o_1)$$

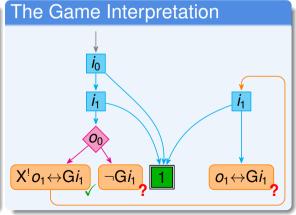




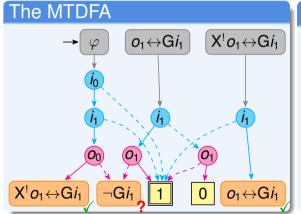
Let's develop this one next.

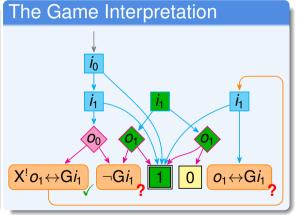
$$\varphi = (i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$$



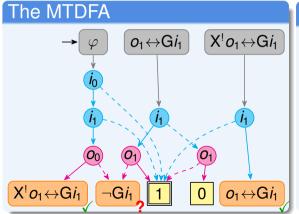


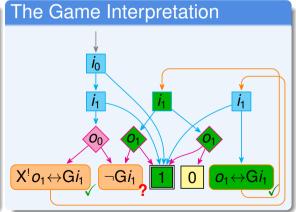
$$\varphi = (i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$$



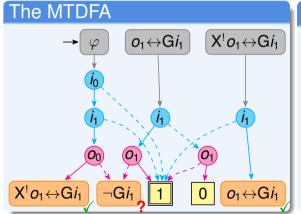


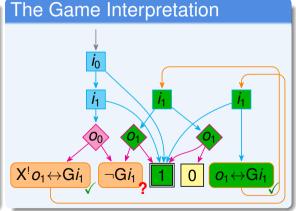
$$\varphi = (i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$$



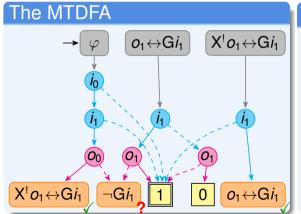


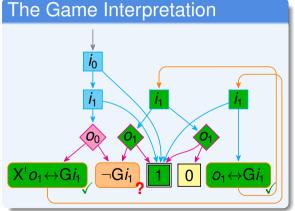
$$\varphi = (i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$$



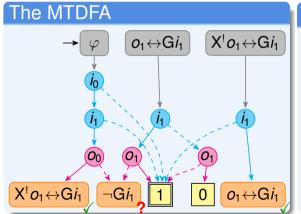


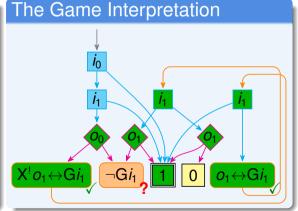
$$\varphi = (i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$$



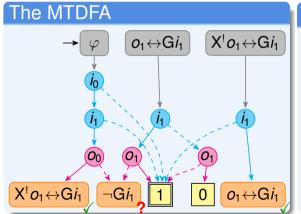


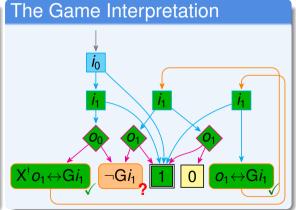
$$\varphi = (i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$$





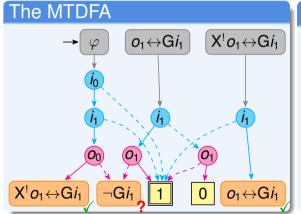
$$\varphi = (i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$$

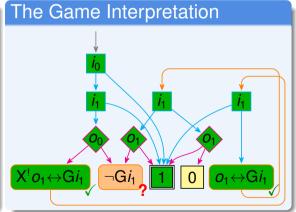




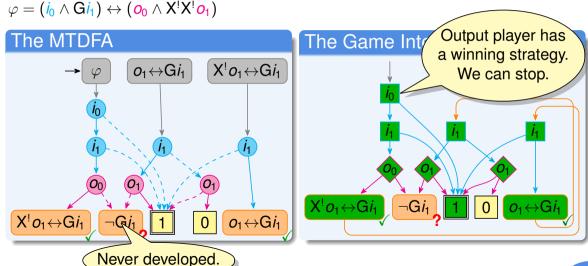
Building the MTDFA & Solving the Game On-The-Fly

$$\varphi = (i_0 \wedge Gi_1) \leftrightarrow (o_0 \wedge X^!X^!o_1)$$





Building the MTDFA & Solving the Game On-The-Fly



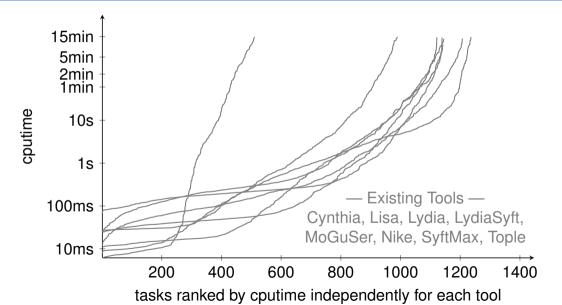
LTL_f Synthesis with MTDFA

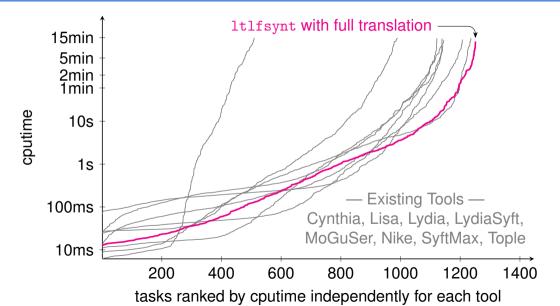
Implemented in two new tools distributed with Spot 2.14 •••••

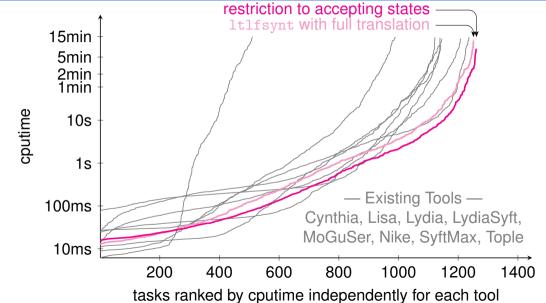


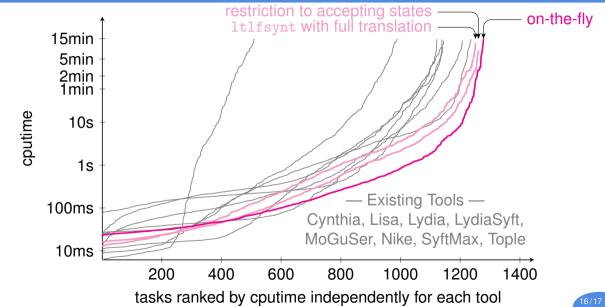
What we Suggest

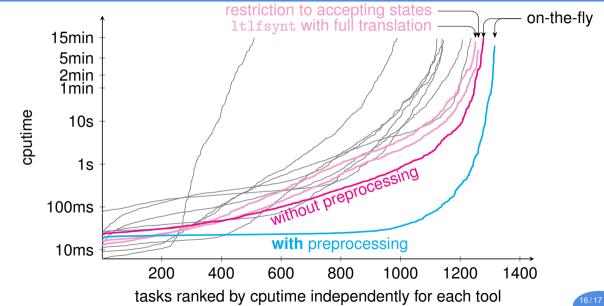
- Direct translation from LTL_f to MTBDD, building the MTDFA one state at a time. √
- ② Solving the game on the MTDFA directly. √
- Ooing those on-the-fly. √











Conclusion

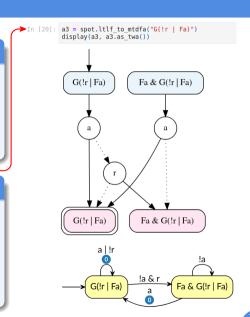
Efficient LTL_f tools to build upon

Distributed with Spot 2.14:

- ▶ ltlf2dfa
- ► ltlfsynt (won SyntComp'25)
- ► C++ & Python APIs available

Ideas to take away

- MTBDDs are great for deterministic automata with propositional alphabets.
- Such automata can be interpreted as games at the level of MTBDD nodes (deciding one proposition at a time).



Warp Zone

```
    Title
    Reactive Synthesis
    Text-Book Approach
    Stopping on Final States
    MTBDD/MTDFA
    Outline
    MTDFA as game
    LTL<sub>f</sub>→MTBDD example
    LTL<sub>f</sub>→MTBDD formal
    LTL<sub>f</sub>→MTDFA
    Accepting Terminals
    On-the-Fly
    Benchmark
    Conclusion
    Preprocessings
    Propositional Equivalence
```

Preprocessings

Simplify specification using polarity of propositions

- If an output proposition is always positive/negative in the specification, replace it by \top/\bot .
- If an input proposition is always positive/negative in the specification, replace it by \bot/\top .

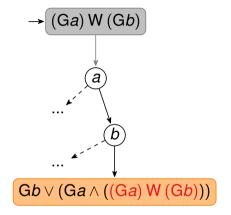
Example: $G(i \rightarrow o)$ becomes $G(\top \rightarrow \top) \equiv \top$.

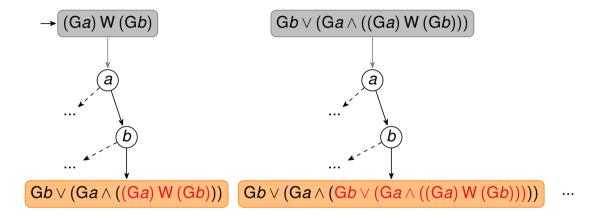
Use cheap rewritings to reduce number of MTBDD operations

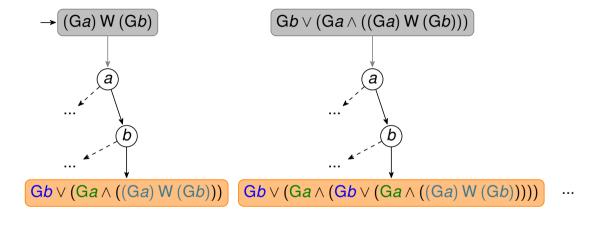
$$X\alpha \wedge X\beta \rightsquigarrow X(\alpha \wedge \beta), \qquad (\alpha \to \beta) \wedge (\alpha \to \gamma) \rightsquigarrow \alpha \to (\beta \wedge \gamma), \qquad \dots$$

Split specification into output disjoint specifications when possible

If Ψ_1 and Ψ_2 are output-disjoint, and admit controllers that agree on accepting lengths, then $\Psi_1 \wedge \Psi_2$ can be solved as two independent problems.







$$p_3 \vee (p_2 \wedge p_1)$$

$$p_3 \vee (p_2 \wedge (p_3 \vee (p_2 \wedge p_1)))$$

